

Notice of Allowability

Application No.

10/068,879

Examiner

VIJAY SHANKAR

Applicant(s)

NAITO, KEIJIRO

Art Unit

2629

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to Examiner's Amendment on 07-20-06.
2. ☒ The allowed claim(s) is/are 5-7, 9-11, 13-15, 17-18, Renumbered as 1-11.
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☒ All b) ☐ Some* c) ☐ None of the:
 1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

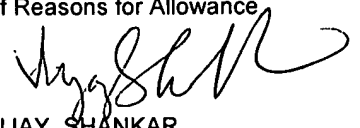
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date _____
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____


VIJAY SHANKAR
Primary Examiner
Art Unit: 2629

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with 7-18-2006 on Joel Armstrong.

The application has been amended as follows:

Claim 13. (Amended) A liquid crystal device having multiple pixels, the liquid crystal device comprising:

an input terminal that receives a display signal including a predetermined signal used for generating a common signal and multiple pixel signals to be supplied to the multiple pixels, wherein the predetermined signal is embedded into a predetermined period between a group of pixel signals and another group of pixel signals in the display signal, and the common signal is to be commonly supplied to the multiple pixels;

a common signal line that is commonly connected to the multiple pixels;
and

a common signal line driving circuit that generates the common signal in response to the predetermined signal included in the display signal input from the input terminal and supplies the generated common signal to the common signal line,

wherein the predetermined period is part of a vertical scanning period except an effective vertical scan period, and

wherein the common signal line driving circuit is a sample/hold circuit that samples the predetermined signal included in the display signal input from the input terminal and outputs the predetermined sampled signal as the common signal.

Allowable Subject Matter

2. Claims 5-7, 9-11, 13-15, 17-18 are allowed. However, Claims 1-4,8,12,16,19-20 are cancelled.

3. The following is an examiner's statement of reasons for allowance: The prior arts Hoshi, Saishu et al, Hirakata, Iba et al does not teach a liquid crystal device having multiple pixels, the liquid crystal device comprising: an input terminal that receives a display signal including a predetermined signal used for generating a common signal and multiple pixel signals to be supplied to the multiple pixels, wherein the predetermined signal is embedded into a predetermined period between a group of pixel signals and another group of pixel signals in the display signal, and the common signal is to be commonly supplied to the multiple pixels; a common signal line that is commonly connected to the multiple pixels; a common signal line driving circuit that generates the common signal in response to the predetermined signal included in the display signal input from the input terminal and supplies the generated common signal to the common signal line; wherein the predetermined period is part of a vertical scanning period except an

effective vertical scan period, and wherein the common signal line driving circuit is a sample/hold circuit that samples the predetermined signal included in the display signal input from the input terminal and outputs the predetermined sampled signal as the common signal as claimed in Claim 13.

4. The prior arts Hoshi, Saishu et al, Hirakata, Iba et al, does not teach a liquid crystal device having multiple pixels, the liquid crystal device comprising: an input terminal that receives a display signal including a predetermined signal used for generating a common signal and multiple pixel signals to be supplied to the multiple pixels, wherein the predetermined signal is embedded into a predetermined period between a group of pixel signals and another group of pixel signals in the display signal, and the common signal is to be commonly supplied to the multiple pixels; a common signal line that is commonly connected to the multiple pixels; a common signal line driving circuit that generates the common signal in response to the predetermined signal included in the display signal input from the input terminal and supplies the generated common signal to the common signal line; wherein the predetermined period is part of a horizontal scanning period except an effective horizontal scan period, and wherein the common signal line driving circuit is a sample/hold circuit that samples the predetermined signal included in the display signal input from the input terminal and outputs the predetermined sampled signal as the common signal as claimed in Claim 9.

5. The prior arts Hoshi, Saishu et al, Hirakata, Iba et al does not teach a liquid crystal device having multiple pixels, the liquid crystal device comprising: **an input terminal that receives a display signal including a predetermined signal used for generating a common signal and multiple pixel signals to be supplied to the multiple pixels, wherein the predetermined signal is embedded into a predetermined period between a group of pixel signals and another group of pixel signals in the display signal, and the common signal is to be commonly supplied to the multiple pixels; a common signal line that is commonly connected to the multiple pixels; a common signal line driving circuit that generates the common signal in response to the predetermined signal included in the display signal input from the input terminal and supplies the generated common signal to the common signal line; wherein the common signal line driving circuit is a sample/hold circuit that samples the predetermined signal included in the display signal input from the input terminal and outputs the predetermined sampled signal as the common signal as claimed in Claim 5.**

The closest prior art, Hoshi, Saishu et al, Hirakata, Iba et al, either singularly or in combination, fail to anticipate or render the above bold and underlined limitations obvious.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to VIJAY SHANKAR whose telephone number is (571) 272-7682. The examiner can normally be reached on M-F 7:00 am - 4:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, BIPIN SHALWALA can be reached on (571) 272-7681. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.



VIJAY SHANKAR
Primary Examiner
Art Unit 2629

VS